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APPLICANTS Terunao Hanaoka, Fujimi-machi, JAPAN; Kenji Wada, Fujimi-machi, JAPAN;				
** CONTINUING DATA ***** This application is a DIV of 09/870,710 06/01/2001 PAT 6,720,661				
** FOREIGN APPLICATIONS ***** JAPAN 2000-166104 06/02/2000 JAPAN 2000-338737 11/07/2000 JAPAN 2001-125581 04/24/2001				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/30/2004				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 23
INDEPENDENT CLAIMS 1				
ADDRESS 25944				
TITLE SEMICONDUCTOR DEVICE, METHOD OF FABRICATING THE SAME, STACK-TYPE SEMICONDUCTOR DEVICE, CIRCUIT BOARD AND ELECTRONIC INSTRUMENT				
FILING FEE RECEIVED 824	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	